IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 040301/0487

OCT 2 7 1999 patent application of

Serial No. 08/997,035

Group Art Unit: 2819

Filed: December 23, 1997

Examiner: D. Le

FOI. SEMICONDUCTO

SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE ARRAY AND

STANDARD CELL

PROPOSED CHANGES TO THE DRAWINGS

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Applicants propose to amend Figure 8 as shown in red on the attached copy. With the Examiner's approval, the changes will be made to the formal drawings in due course.

Respectfully submitted,

October 27, 1999

Date

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